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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chow, Lap-Wai; et al.) Examiner: Lee, E	ugene
Serial No.:	10/735,841) Art Unit: 2815	
Filed:	December 12, 2003) Our Ref: B-4425NP	621267-1
For: "Integrated circuit modification using well implants") Date: February	19, 2007
	•) Re: Amendme) after Final	ent and Response

AMENDMENT AND RESPONSE AFTER FINAL

Mail Stop After Final Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is in reply to the final Office Action mailed on November 27, 2006, an initial response to which is due no later than

February 27, 2006.

Since this paper is filed in response to a Final Office Action, the Applicants submit that this paper is a reply under 37 CFR 1.116 and request expedited processing of this response after final rejection as described in MPEP 714.13. Please amend the above-identified application as described below and consider the following remarks. All amendments and remarks herein are made without prejudice.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.